

Shenzhen Leadtek Electronics Co.,Ltd

PRODUCT SPECIFICATION

TFT-LCD HDMI MODULE

Module No: LTK040FTIIT61-V0

Preliminary Specification

Approval Specification

| Designed by | Checked by | Approved by |
|-------------|-------------|--------------|
| <i>jona</i> | <i>Jams</i> | <i>Kevin</i> |

Final Approval by Customer

| Approved by | Comment |
|-------------|---------|
| | |

※The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

2. General Description

| N0 | Item | Specification | Unit | Remark |
|----|--------------------|-------------------------------|-------|--------|
| 1 | LCD Size | TFT"4.0 | inch | - |
| 2 | Panel Type | IPS | - | - |
| 3 | Display Resolution | 480 x RGB x1080 | pixel | - |
| 4 | Display mode | Normal Black | - | - |
| 5 | Display colors | 16.7M | - | - |
| 6 | Viewing Direction | ALL | - | - |
| 7 | CG+LCM Module Size | 47.10 (H) x101.90(V) x2.92(T) | mm | Note |
| 8 | Active Area | 42.00(H) x 94.50(V) | mm | Note |
| 9 | Pixel Pitch | 0.0875x0.0875 | mm | - |
| 10 | Weight | - | g | - |
| 11 | Driver IC | JD9365TX | bit | - |
| 12 | Light Source | White LED | - | - |
| 13 | Interface | MIPI_TDDI 4 Lanes | - | - |

3. Mechanical Drawing

4.0 Interface Pin Connection

| N0 | Symbol | Function |
|----|-------------|---|
| 1 | GND | Power ground. |
| 2 | MIPI_D0P | MIPI_DP0+ are differential data signal line |
| 3 | MIPI_D0N | MIPI_DP0- are differential data signal line |
| 4 | GND | Power ground. |
| 5 | MIPI_D1P | MIPI_DP1+ are differential data signal line |
| 6 | MIPI_D1N | MIPI_DP1- are differential data signal line |
| 7 | GND | Power ground. |
| 8 | MIPI_CLKP | CLOCK Lane positive-end input pin |
| 9 | MIPI_CLKN | CLOCK Lane engative-end input pin |
| 10 | GND | Power ground. |
| 11 | MIPI_D2P | MIPI_DP2+ are differential data signal line |
| 12 | MIPI_D2N | MIPI_DP2- are differential data signal line |
| 13 | GND | Power ground. |
| 14 | MIPI_D3P | MIPI_DP3+ are differential data signal line |
| 15 | MIPI_D3N | MIPI_DP3- are differential data signal line |
| 16 | GND | Power ground. |
| 17 | GND | Power ground. |
| 18 | IOVCC(1.8V) | A supply voltage to the digital circuit. (1.8V) |
| 19 | IOVCC(1.8V) | A supply voltage to the digital circuit. (1.8V) |
| 20 | TP-VDD | CTP power supply input.(CTP-VDD-3.3V) |
| 21 | TP-SDA | CTP I2C data line.(CTP-SDA-1.8V) |
| 22 | TP-SCL | CTP I2C clock line.(CTP-SCL-1.8V) |
| 23 | TP-GND | CTP ground. |
| 24 | RSTB | Reset signal (Low: Active). |
| 25 | NC | Not connect |
| 26 | TP-INT | CTP interrupt line. |
| 27 | GND | Power ground. |
| 28 | LED- | LED cathode. |

| | | |
|----|-----------|---|
| 29 | LED- | LED cathode. |
| 30 | GND | Power ground. |
| 31 | NC | Not connect |
| 32 | GND | Power ground. |
| 33 | GND | Power ground. |
| 34 | NC | Not connect |
| 35 | LED+ | LED anode. |
| 36 | LED+ | LED anode. |
| 37 | GND | Power ground. |
| 38 | VDD(3.3V) | A supply voltage to the digital circuit. (3.3V) |
| 39 | VDD(3.3V) | A supply voltage to the digital circuit. (3.3V) |
| 40 | TP-RESET | CTP reset line. |

5.0 Absolute Maximum Ratings

5.1 Electrical Absolute Rating

5.1.1 TFT LCD Module

(Ta=25±2°C)

| Characteristics | Symbol | Min. | Typ. | Max. | Unit | Note |
|------------------------|--------|------|------|------|------|------|
| Power Supply Voltage 1 | IOVCC | 1.65 | 1.8 | 1.95 | V | |
| Power Supply Voltage 2 | VDD | 2.8 | 3.0 | 3.3 | V | |

Note (1) Stresses above those listed under "Absolute Maximum Rating" may cause permanent damage to the device. These are stress ratings only. Functional operation of this device at indicated in the operational sections(6.1) of this specification.

5.2 Environment Absolute Rating

| Item | Symbol | Min. | Max. | Unit | Note |
|-----------------------|--------|------|------|------|------|
| Operating Temperature | Topa | -20 | 70 | °C | |
| Storage Temperature | Tstg | -30 | 80 | °C | |

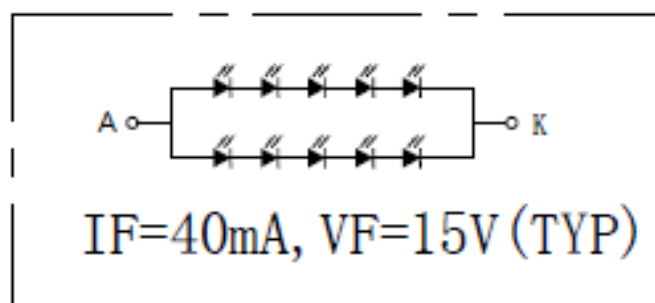
5.3 Back-light Unit:

| PARAMETER | Sym. | Min. | Typ. | Max. | Unit | Test Condition | Note | |
|-------------------|------|-------|-------|------|------|----------------|------|--|
| LED Current | IF | – | 40 | – | mA | – | – | |
| LED Voltage | VF | – | 15 | 16.5 | V | – | – | |
| CG+LCM Brightness | Lv | – | 250 | – | Nits | @CA310 | – | |
| Life Time | | – | 30000 | – | Hr. | I ≤ 40mA | – | |
| Color | | White | | | | | | |

Note (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

(2)Ta=25±2°C

(3)Test condition: LED Current 40mA



6.0 Power-up Sequence Example

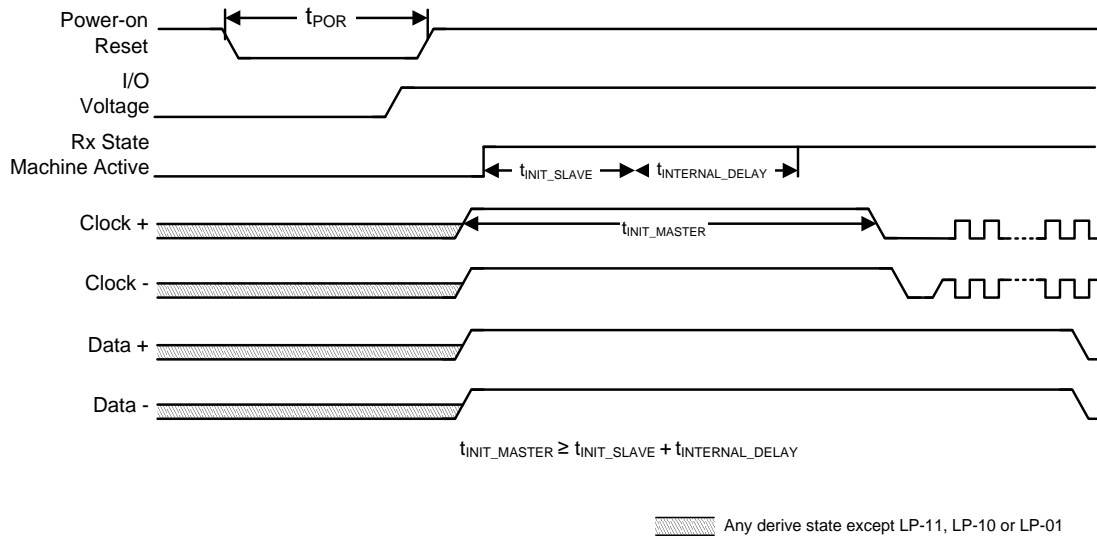


Figure : Peripheral Power-Up Sequencing Example

6.1 DC characteristics

($T_A = -20 \sim 80 \text{ }^\circ\text{C}$, $V_{CIP} = 2.5 \sim 3.7\text{V}$, $I_{OVCC} = 1.65 \sim 1.95\text{V}$)

| Item | Symbol | Condition | Min. | Typ. | Max. | Unit |
|--|-------------------------|--|----------------|-------|----------------|---------------|
| IOVCC | V_{IN} | Interface Supply Voltage | 1.65 | - | 1.95 | |
| VCCH | V_{IN} | High speed interface Supply Voltage | 1.65 | - | 1.95 | |
| Input high voltage | V_{IH} | IOVCC= 1.65 ~ 1.95V VCIP= 2.5 ~ 4.8V VCI= 2.5 ~ 4.8V | 0.7 I_{OVCC} | - | IOVCC | V |
| Input low voltage | V_{IL} | | 0 | - | 0.3 I_{OVCC} | V |
| VPP | V_{IH} | VPP | 8V | 8.25V | 8.5V | V |
| | V_{IL} | | | | | |
| Output high voltage (SDO, LEDPWM) | V_{OH1} | $I_{OH} = -1.0 \text{ mA}$ | 0.8 I_{OVCC} | - | IOVCC | V |
| Output low voltage (SDO, LEDPWM) | V_{OL1} | IOVCC= 1.65 ~ 1.95V $I_{OL} = 1.0 \text{ mA}$ | 0 | - | 0.2 I_{OVCC} | V |
| Logic High level input current | I_{IH} | VSYNC, HSYNC | - | - | 1 | μA |
| | | RESX, DCX_SCL, CSX, RDX, WRX_SCL | - | - | 1 | μA |
| | I_{IHD} | D[7...0], SDI, DCX | - | - | 1 | μA |
| | | D[7...0] | - | - | 1 | μA |
| Logic Low level input current | I_{IL} | VSYNC, HSYNC | -1 | - | | μA |
| | | RESX, DCX, CSX, RDX, WRX_SCL | -1 | - | | μA |
| | I_{ILD} | D[7...0], SDI, DCX | -1 | - | | μA |
| | | D[7...0] | -1 | - | | μA |
| Current consumption standby mode (VCIP/VCI-VSSD) | $I_{ST(VDD)}$ | VCIP/VCI=2.8V, IOVCC=1.8V $T_A = 25^\circ\text{C}$ | - | TBD | - | μA |
| Current consumption standby mode (VCCD/IOVCC-VSSD) | $I_{ST(VCCD/IOVCC)}$ | | - | - | 2 | mA |
| Current consumption during Deep-standby mode (VCIP/VCI-VSSD) | $I_{DP-ST(VDD)}$ | VCIP/VCI=2.8V, IOVCC=1.8V $T_A = 25^\circ\text{C}$ | - | TBD | - | μA |
| Current consumption during Deep-standby mode (VCCD/IOVCC-VSSD) | $I_{DP-ST(VCCD/IOVCC)}$ | | - | TBD | - | μA |

- Note:**
1. The VOTP pin is open on normal mode and in used while OTP programming condition.
 2. The GRAM data is eliminated under the Deep standby mode.
 3. TP_RESET set low for standby mode.

7.0. AC characteristics

7.1.Reset input timings

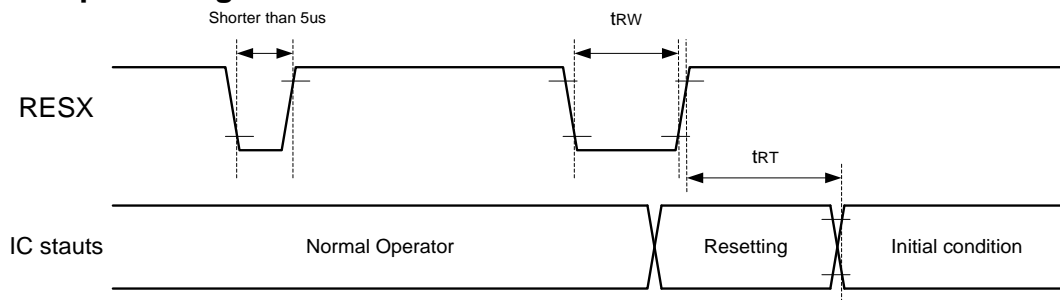


Figure 11.1: Reset input timings

| Symbol | Parameter | Related pins | Min. | Max. | Unit |
|----------|------------------------------------|--------------|------|--------------------|---------|
| t_{RW} | Reset pulse width ⁽²⁾ | RESX | 10 | - | μ s |
| t_{RT} | Reset complete time ⁽³⁾ | - | - | 5 (Note 5) | ms |
| | | - | - | 120 (Note 6, 7) | ms |

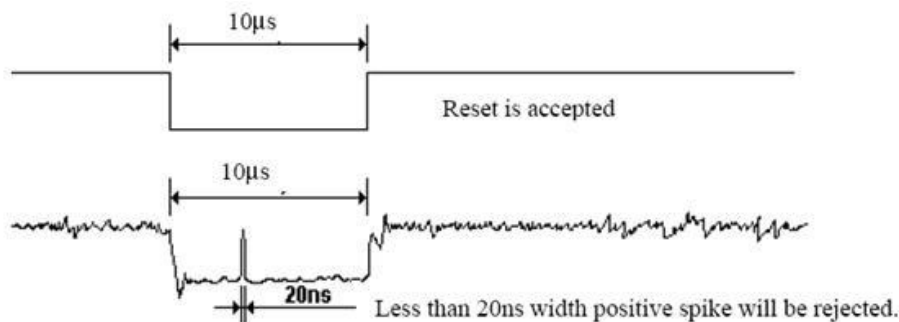
Note: (1) The reset complete time also required time for loading ID bytes from OTP to registers. This loading is done every time when there is HW reset cancel time (t_{RT}) within 5 ms after a rising edge of RESX.

(2) Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below.

| RESX Pulse | Action |
|----------------------------------|----------------|
| Shorter than 5 μ s | Reset Rejected |
| Longer than 10 μ s | Reset |
| Between 5 μ s and 10 μ s | Reset Start |

(3) During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode) and then returns to Default condition for H/W reset.

(4) Spike Rejection also applies during a valid reset pulse as shown below:



(5) When Reset is applied during Sleep In Mode.

(6) When Reset is applied during Sleep Out Mode.

(7) It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

(8) After Sleep Out Command, it is necessary to wait 120msec then send RESX.

Table : Reset timings

8. Optical Specification

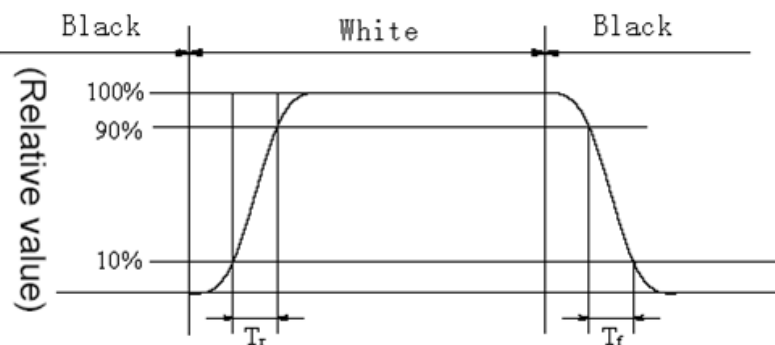
| Item | Symbol | Condition | Values | | | Unit | Notes |
|---|-----------------|----------------------------------|----------|--------|----------|------|-----------------|
| | | | Min. | Typ. | Max. | | |
| Transmittance | Tr | Perpendicular@25°C(KSF type LED) | / | 3.7 | / | % | Base on C light |
| Contrast Ratio | CR | Perpendicular@25°C | 800 | 1000 | / | / | Note 1 |
| Viewing Angle | L/R/U/D | CR≥10@25°C | / | 80 | / | ° | |
| Response time (Rising+Falling) | T _{RT} | T _a =25°C | 30 | 35 | / | ms | Note 2 |
| Color Gamut | NTSC | CIE 1931 | / | 70 | / | % | Base on C light |
| Color chromaticity (CIE1931) @25°C Perpendicular | W _x | x _w | Typ-0.03 | 0.2926 | Typ+0.03 | / | Base on C light |
| | W _y | y _w | | 0.3256 | | | |
| | R | x _R | | - | | | |
| | | y _R | | - | | | |
| | G | x _G | | - | | | |
| | | y _G | | - | | | |
| | B | x _B | | - | | | |
| | | y _B | | - | | | |

Note 1: Definition of contrast ratio

$$\text{Contrast Ratio(CR)} = \frac{\text{Luminance When LCD is White}}{\text{Luminance When LCD is Black}}$$

Note 2: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (Tr) is the time between photo detector output intensity changed from 10% to 90%. And fall time (Tf) is the time between photo detector output intensity changed from 90% to 10%.



9. Reliability test items

| No. | Test Item | Test Condition | Notes |
|-----|------------------------------------|---|--|
| 1 | High Temp. Storage | +80°C / 48H | 1. Functional test isOK. Missing Segment,short, unclear segment non-display,display abnormally and liquid crystal leakare un-allowed. 2. No low temperature bubbles,end seal loose andfall, frame rainbow. |
| 2 | Low Temp. Storage | -30°C / 48H | |
| 3 | High Tempe. Operating | +70°C / 48H | |
| 4 | Low Tempe. Operating | -20°C / 48H | |
| 5 | High Temperature /Humidity storage | 50°C x 90%RH /48H | |
| 6 | Thermal and cold shock | Static state, -20°C (30min) ~60°C (30min), 50 cycles | |
| 7 | ESD test | ± 4KV,Human Body Mode, 150pF/330Ω; ±8KV, Air Mode, 150pF/330Ω; | |

Note: All tests above are practiced at module type.

There is no display function NG issue occurred, All the cosmetic specification is judged before the reliability stress.

10.0 General Precaution

10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

10.2 Assembly Precaution

- 1、 Please use the mounting hole on the module side in installing and do not bending or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.
- 2、 Please design display housing in accordance with the following guide lines.
- 3、 Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
- 4、 Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. The clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- 5、 Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- 6、 Please do not press any parts on the rear side such as source IC, gate IC, and FPC during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- 7、 Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- 8、 Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change. 11.2.7 Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

10.3 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. Leadtek does not warrant the module, if customers disassemble or modify the module.

10.4 Breakage of LCD Panel

- 1、 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 2、 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 3、 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 4、 Handle carefully with chips of glass that may cause injury, when the glass is broken.

10.5 Absolute Maximum Ratings and Power Protection Circuit

- 1、 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 2、 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 3、 It's recommended employing protection circuit for power supply.

11.6 Operation

- 1、 Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 2、 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 3、 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 4、 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

10.6 Static Electricity

- 1、 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 2、 Because LCD module uses CMOS-IC on TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- 3、 Persons who handle the module should be grounded through adequate methods.

10.7 Disposal

When disposing LCD module, obey the local environmental regulations.

10.8 OTHERS

- 1、 A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight land strong UV rays.
- 2、 Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- 3、 For the packaging box, please pay attention to the followings:
- 4、 Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
- 5、 Please do not pile them up more than 6 boxes. (They are not designed so.) And please do not turn over.
- 6、 Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
- 7、 Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)

11.0 Packing form-TBD



深圳市丽台电子有限公司

Shenzhen Leadtek Electronics Co.,Ltd

Incoming Inspection Standard

品质允收标准

Model N0. /产品型号: Applicable to Leadtek Touch Display Screen

Updated Date /生效日期: 2025.04.01

Version / 版本号: V0

Record of Revision /修订履历.

| Version /版本号 | Revision Record /修订内容 | Reviser /修订人 | Revision Date /修订日期 |
|--------------|-----------------------|--------------|---------------------|
| V0 | 首发 / Initial release | Green | 2025.04.01 |
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1、Scope of application /适用范围.

适用于丽台电子触摸显示屏/ Applicable to Leadtek Touch Display Screen.

2、Inspection conditions and environment / 检验条件与环境.

2.1、Inspection Conditions / 检验条件:

2.1.1、Inspection Distance / 检测距离: 35cm ±5cm.

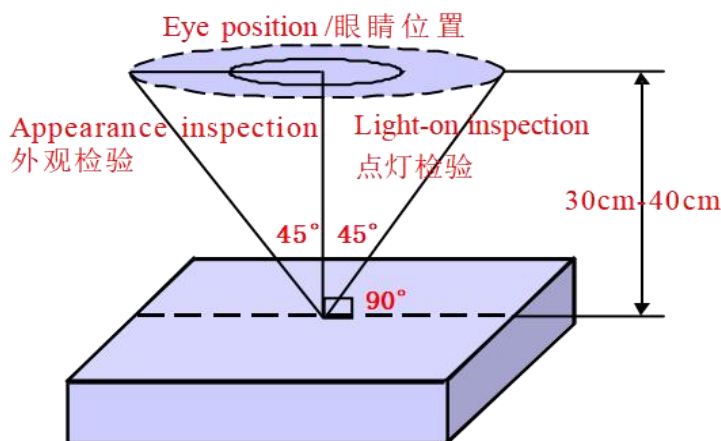
2.1.2、Inspection time /检验时间: Displays performance test /功能测试: 2~3S /Image, Appearance Inspection / 外观检验: 8~10S.

2.1.3、Inspection the viewing angle /检验视角:

Display Inspection Angle /显示检验角度: ±45°.

Appearance Inspection Angle /外观检验角度: ±45°.

Eye position /眼睛位置:



(Perpendicular to LCD panel surface /垂直于LCD表面)

2.2、Inspection environment /检验环境:

| | | |
|------------------|------------------------------|--------------|
| Temperature / 温度 | | 25±5°C |
| Humidity / 湿度 | | 55±5%RH |
| Brightness | Appearance Inspection / 外观检验 | 800~1000 Lux |
| 亮度 | Display Inspection / 功能检验 | 200~300 Lux |

2.3、Sampling conditions / 抽样方式.

| | | |
|----------------------|---------------------|---|
| Sampling Plan / 抽样计划 | | GB/T 2828.1- 2003 |
| | | Batch single sampling/批量单次抽样 |
| | | General inspection level: II/一般检验水平: 二级 |
| AQL | Major Defect / 主要缺陷 | 0.25 |
| | Minor Defect / 次要缺陷 | 0.65 |

3、Terms and definitions / 术语和定义.

3.1、Defect classification / 缺陷分类:

3.1.1、Major defects / 主要缺陷: Defects that cause the product to fail or reduce the usability of the product / 引起产品功能失效和减少产品的有效使用与操作的缺陷.

3.1.2、Minor defects / 次要缺陷: Defects that do not affect the functionality and effective use and operation of the product / 不影响产品功能和有效使用与操作的缺陷.

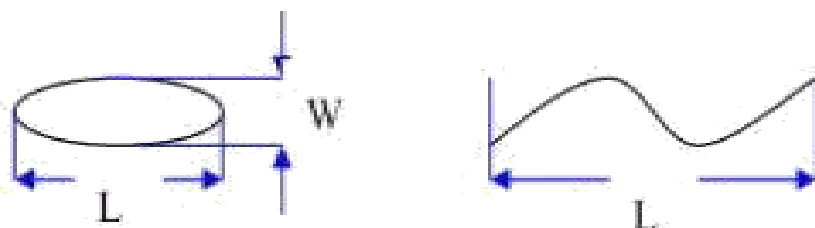
3.2、Point defects / 点状缺陷:

The size of a punctate defect is defined by the diameter D, and the average diameter of the defect is $D=1/2(W+L)$ / 点状缺陷由直径 D 定义大小, 缺陷的平均直径 $D=1/2(W+L)$.

3.3、line defects / 线状缺陷:

When defect size $L \geq 2W$, the defect count as liner type defect. Size of linear defect is defined by length (L) and the maximum width (W)

当缺陷尺寸 $L \geq 2W$ 时, 被视为线状缺陷, 线状缺陷是由长度 (L) 和最大宽度 (W) 定义的.



3.4、LCD sub-pixels / LCD 子像素点:

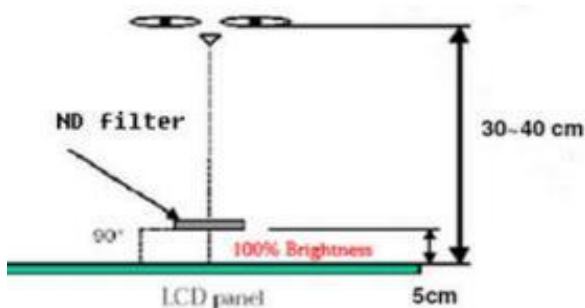
The sub-pixel defect area is greater than 50% of the LCD sub-pixel area, and is visible through ND5% masking

子像素点缺陷面积大于 50% LCD 子像素面积, 且透过 ND5%遮盖是可见的.

3.5、ND card test method / ND 卡检验方法:

Hold the ND card about 5cm above the display area, with your eyes 30-40cm away from the panel, and observe for 2~3 seconds

在显示区域上方大约 5cm 处握住 ND 卡, 眼睛距离面板 30-40cm, 观察 2~3 秒.



3.6、Surface substances that can be cleaned are not considered defects (e.g. finger prints on the protective film, dust particles)

可以被清洁干净的表面物质不视为缺陷 (如保护膜上的手指印, 尘粒) .

3.7、Defects that can be covered by the material and are not visible to the eye, and do not affect the function and use are not considered defects

能被物料覆盖目视不可见, 且不影响功能与使用的缺陷不视为缺陷.

3.8、AA shows that damage to the glass outside the area does not affect the effective line and does not expand the loss is acceptable

AA 显示区域以外的玻璃损伤, 不影响有效线路和不会在扩大损失的是可以接受的.

3.9、Issues not specified or defined in this document shall be dealt with through friendly negotiation between the parties / 本文件中未规定或定义的问题, 双方友好协商处理.

4.0、Inspection standards /检验标准:

4.1、Structural size standards / 结构尺寸标准:

| Measurement items /测量项目 | | Specification /规格 | Remark /备注 |
|---------------------------------------|----------|-------------------|--|
| Name/名称 | Unit /单位 | Tolerance /公差 | |
| Outside dimension: Length 尺寸: 长 | mm /毫米 | 0.10mm~0.3mm | Please refer to the product specification for detailed dimensions and tolerances 详细的尺寸规格和公差请参考产品规格书 |
| Outside dimension: Width 尺寸: 宽 | mm /毫米 | 0.10mm~0.3mm | |
| Outside dimension: Thickness 尺寸: 厚 | mm /毫米 | 0.20mm~0.50mm | |

4.2、Appearance Inspection standards:

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

| Material 检验区域 | Inspection items 检验项目 | Product size 适用尺寸 | Inspection standards 检验规格 | Category 缺陷类别 | |
|------------------|--------------------------|----------------------|---|------------------|----|
| LCD | ITO | Full size 全尺寸 | ITO can't open circuit, short circuit, ITO notch cannot exceed 70% of width. ITO 不能有开路 and 短路, ITO 缺口不能超过宽度的70%. | MA | |
| | Corners broken 崩边/崩角 | Full size 全尺寸 | 1、 It cannot affect the appearance of valid routes and functions; 不能影响有效线路和功能外观. | MA | |
| | | | 2、 There must be no extensible rips 不能有可延伸性裂纹. | MA | |
| Silicone 硅胶 | Silicone coating 硅胶涂布 | Full size 全尺寸 | 1、 The height cannot exceed the LCD CF surface / 高度不能超过 LCD CF 面. | | MI |
| | | | 2、 No overflow and lack of glue / 不能溢胶 and 缺胶. | | MI |
| | | | 3、 Silicone cannot cover POL and FPC/ 硅胶不能覆盖到 POL and FPC. | | MI |

| Material 检验区域 | Inspection items 检验项目 | Product size 适用尺寸 | Inspection standards 检验规格 | Category 缺陷类别 | |
|---|--------------------------|------------------------------|--|------------------|----|
| PCBA FPC Connector 连接器 | Appearance 表面外观 | Full size 全尺寸 | 1.FPC is not allowed to have drape/bubble/fold / 不允许有披锋/气泡/褶皱. | | MI |
| | | | 2.Surface scratches do not allow copper leakage / 表面划伤不允许出现漏铜. | MA | |
| | | | 3.Cracking is not allowed / 不允许破裂. | MA | |
| | | | 4.Gold finger oxidation is not allowed 不允许金手指氧化. | MA | |
| | | | 5.Chromatic aberration is not allowed 不允许出现色差. | MA | |
| | Components 元器件 | Full size 全尺寸 | 1. Damage, missing parts, and incorrect models are not allowed 不允许损伤、缺件、型号错误. | MA | |
| | | | 2.Components need to be insulated with high temperature adhesive (unless not required by the drawings) / 元器件需要有高温胶绝缘保护 (除非图纸无要求) . | | MI |
| | | | 3.The pads need to be glued with high temperature (unless not required by the drawings) / 焊盘需贴高温胶 (除非图纸无要求) . | | MI |
| | Weld 焊接 | Full size 全尺寸 | 1. False soldering, virtual soldering, and tinning are not allowed 不允许假焊、虚焊、连锡. | MA | |
| | | | 2. No solder bead residue is allowed 不允许有锡珠残留. | | MI |
| 3.The pads need to be glued with high temperature (unless not required by the drawings) / 焊盘需贴高温胶 (除非图纸无要求) . | | | | MI | |
| POL 偏光片 | Scratches 划伤 | Under 6 inches 6寸以下 | 1.W≤0.05mm; L≤5mm, Ignore (忽略) . 2.0.05mm < W≤0.10mm ; L≤5mm ; N≤2; DS≥10mm. 3.0.10mm < W; 5mm < L, Not allowable (不允许) . | | MI |
| | | 6~10.0 inches 6寸~10.0寸 | 1.W≤0.07mm; L≤5mm, Ignore (忽略) . 2.0.07mm < W≤0.12mm ; L≤10mm ; N≤3 ; DS≥10mm. 3.0.12mm < W; 10mm < L, Not allowable (不允许) . | | MI |
| | | More than 10 inches 10寸以上 | 1.W≤0.10mm; L≤5mm, Ignore (忽略) . 2.0.10mm < W≤0.15mm ; L≤10mm ; N≤4 ; DS≥10mm. 3.0.15mm < W; 10mm < L, Not allowable (不允许) . | | MI |

| Material 检验区域 | Inspection items 检验项目 | Product size 适用尺寸 | Inspection standards 检验规格 | Category 缺陷类别 | |
|------------------|----------------------------------|------------------------------|--|------------------|----|
| POL 偏光片 | Bubbles | Under 6 inches 6寸以下 | 1.D≤0.15mm, Ignore (忽略) . 2.0.15mm < D≤0.30mm; N≤2; DS≥10mm. 3.D > 0.30mm, Not allowable (不允许) . | | MI |
| | | More than 6 inches 6寸以上 | 1.D≤0.20mm, Ignore (忽略) . 2.0.20mm < D≤0.40mm; N≤3; DS≥10mm. 3.D > 0.40mm, Not allowable (不允许) . | | MI |
| | Bubbles around the edges 边缘气泡 | Full size 全尺寸 | 1.Within 1/2BM of the display area, it is not allowed 显示区往外 1/2BM 区域内, 不允许. 2.The display area is 1/2 outside the BM area, and it is not controlled 显示区往外 1/2BM 区域以外, 不管控. | | MI |
| | Point defects Embossing | Under 6 inches 6寸以下 | 1.D≤0.15mm, Ignore (忽略) . 2.0.15mm < D≤0.30mm; N≤2; DS≥10mm. 3.D > 0.30mm, Not allowable (不允许) . | | MI |
| | | 6~10.0 inches 6寸~10.0寸 | 1.D≤0.20mm, Ignore (忽略) . 2.0.20mm < D≤0.40mm; N≤3; DS≥10mm. 3.D > 0.40mm, Not allowable (不允许) . | | MI |
| | | More than 10 inches 10寸以上 | 1.D≤0.25mm, Ignore (忽略) . 2.0.25mm < D≤0.50mm; N≤4; DS≥10mm. 3.D > 0.50mm, Not allowable (不允许) . | | MI |
| | Dirty 脏污 | Full size 全尺寸 | Dirt, finger prints, etc. are not allowed 不允许有脏污、手指印等. | | MI |
| | Warping 起翘 | Full size 全尺寸 | Not allowed 不允许. | | MI |
| | Paste offset 贴附偏位 | Full size 全尺寸 | It is not allowed to exceed the patch tolerance required by the drawing; After TP lamination, it is not allowed to leak the edges of the polarizer 不允许超出图纸要求的贴片公差; 在 TP 贴合后不允许漏偏光片边缘. | | MI |
| | Angle mistake 角度错误 | Full size 全尺寸 | Not allowed 不允许. | | MA |
| Mixture 混料 | Full size 全尺寸 | Not allowed 不允许. | | MA | |

| Material 检验区域 | Inspection items 检验项目 | Product size 适用尺寸 | Inspection standards 检验规格 | Category 缺陷类别 | |
|------------------|-----------------------------------|------------------------------|--|------------------|----|
| TP | Scratches 划伤 | Under 6 inches 6寸以下 | 1.W≤0.05mm; L≤5mm, Ignore (忽略) . 2.0.05mm < W≤0.10mm ; L≤5mm ; N≤2 ; DS≥10mm. 3.0.10mm < W; 5mm < L, Not allowable (不允许) . 4.There is a feeling scratch, Not allowable 有感划伤, 不允许. | | MI |
| | | 6~10.0 inches 6寸~10.0寸 | 1.W≤0.07mm; L≤5mm, Ignore (忽略) . 2.0.07mm < W≤0.12mm ; L≤10mm ; N≤3 ; DS≥10mm. 3.0.12mm < W; 10mm < L, Not allowable (不允许) . 4.There is a feeling scratch, Not allowable 有感划伤, 不允许. | | MI |
| | | More than 10 inches 10寸以上 | 1.W≤0.10mm; L≤5mm, Ignore (忽略) . 2.0.10mm < W≤0.15mm ; L≤10mm ; N≤4 ; DS≥10mm. 3.0.15mm < W; 10mm < L, Not allowable (不允许) . 4.There is a feeling scratch, Not allowable 有感划伤, 不允许. | | MI |
| | Black dots white dots 黑点/白点 | Under 6 inches 6寸以下 | 1.D≤0.15mm, Ignore (忽略) . 2.0.15mm < D≤0.30mm; N≤2; DS≥10mm. 3.D > 0.30mm, Not allowable (不允许) . | | MI |
| | | 6~10.0 inches 6寸~10.0寸 | 1.D≤0.20mm, Ignore (忽略) . 2.0.20mm < D≤0.40mm; N≤3; DS≥10mm. 3.D > 0.40mm, Not allowable (不允许) . | | MI |
| | | More than 10 inches 10寸以上 | 1.D≤0.25mm, Ignore (忽略) . 2.0.25mm < D≤0.50mm; N≤4; DS≥10mm. 3.D > 0.50mm, Not allowable (不允许) . | | MI |
| | OCA Bubbles 气泡 | Full size 全尺寸 | Not allowed 不允许. | | MI |
| | Corners broken 崩边/崩角 | Full size 全尺寸 | 1.Product front /产品正面: Edge and corner chipping is not allowed / 崩角、崩边不允许 2.Product back /产品背面: X≤0.5, Y≤0.5, Z≤1/2T; N≤4; DS≥10mm. | MA | |
| | Silk screen 丝印 | Full size 全尺寸 | The silk screen is clear, complete and correct 丝印清晰、完整、内容正确. | | MI |

| Material 检验区域 | Inspection items 检验项目 | Product size 适用尺寸 | Inspection standards 检验规格 | Category 缺陷类别 | |
|------------------|--|----------------------|--|------------------|----|
| TP | Dirty 脏污 | Full size 全尺寸 | Uncleanable dirt, Not allowable. 不可擦拭的脏污, 不允许. | | MI |
| | Broken 破损 | Full size 全尺寸 | Not allowable. 不允许. | | MI |
| | Ink color aberration 油墨色差 | Full size 全尺寸 | $\Delta E > 1$, Not allowable (不允许). | | MI |
| | Cover pinholes 针孔 | Full size 全尺寸 | 1.D \leq 0.20mm, N \leq 2, DS \geq 10mm, allowable 2.D > 0.20mm, intensive pinholes (密集型针孔), Not allowable (不允许). | | MI |
| | Paint off 掉漆 | Full size 全尺寸 | Touch-up on the back of the cover is allowed, and the touch-up area cannot exceed 2.0mm in diameter / 允许在盖板背面补漆, 补漆面积不 能超过直径 2.0mm. | | MI |
| BL 背光 | Backlight separation 背光分离 | Full size 全尺寸 | Not allowable 不允许. | | MI |
| | Deformed 变形 | Full size 全尺寸 | Measured using a plug gauge, If the deformation exceeds 0.3mm, NG is judged 使用塞规测量, 形变超过 0.3mm, 判定 NG. | | MI |
| | Iron frame Oxidation /abscission 铁框氧化/脱落 | Full size 全尺寸 | Not allowable 不允许. | | MI |
| | Dirt/adhesive residue/solder beads 脏污/残胶/锡珠 | Full size 全尺寸 | Not allowable 不允许. | | MI |
| | Inkjet/barcode/ QR code 喷码/条码/二维 码 | Full size 全尺寸 | The inkjet code is clear and complete, the barcode and QR code can be scanned normally, and the content and format meet the requirement / 喷码清晰完整、条码和二维码 可正常扫描, 内容和格式与要求相符. | | MI |
| | Auxiliary materials 辅料 | Full size 全尺寸 | Accessories (vinyl, double-sided tape, insulating glue, etc.) are not allowed to be missed, misguided, defective, etc 辅料(黑胶、双面胶、绝缘胶等)不允许有漏贴、 贴偏、残缺等. | | MI |

4.3、Functional inspection standards:

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

| Material 检验区域 | Inspection items 检验项目 | Product size 适用尺寸 | Inspection standards 检验规格 | Category 缺陷类别 | |
|----------------------|--|------------------------------|--|------------------|----|
| Display Screen 模组 | Light leakage / Mura 漏光/ Mura | Full size 全尺寸 | 1.Use ND5% filter masking, visual invisibility is OK 使用 ND5%遮盖, 目视不可见即为 OK. 2.If necessary, sign off on the sample 必要时, 签限定样. | | MI |
| | Brightness uniformity 亮度均匀性 | Full size 全尺寸 | Brightness uniformity < 85.0%, Not allowable 亮度均匀性 < 85.0%, 不允许. | MA | |
| | LCD bright spots/dark spots 玻璃亮点/暗点 | Under 6 inches 6寸以下 | 1.D≤0.10mm, Ignore (忽略) . 2.0.10mm < D≤0.20mm; N≤2; DS≥10mm. 3.D > 0.20mm, Not allowable (不允许) . | | MI |
| | | 6~10.0 inches 6寸~10.0寸 | 1.D≤0.15mm, Ignore (忽略) . 2.0.15mm < D≤0.30mm; N≤3; DS≥10mm. 3.D > 0.30mm, Not allowable (不允许) . | | MI |
| | | More than 10 inches 10寸以上 | 1.D≤0.20mm, Ignore (忽略) . 2.0.20mm < D≤0.40mm; N≤4; DS≥10mm. 3.D > 0.40mm, Not allowable (不允许) . | | MI |
| | Backlight black dots/white dots 背光黑点/白点 | Under 6 inches 6寸以下 | 1.D≤0.15mm, Ignore (忽略) . 2.0.15mm < D≤0.30mm; N≤2; DS≥10mm. 3.D > 0.30mm, Not allowable (不允许) . | | MI |
| | | 6~10.0 inches 6寸~10.0寸 | 1.D≤0.20mm, Ignore (忽略) . 2.0.20mm < D≤0.40mm; N≤3; DS≥10mm. 3.D > 0.40mm, Not allowable (不允许) . | | MI |
| | | More than 10 inches 10寸以上 | 1.D≤0.25mm, Ignore (忽略) . 2.0.25mm < D≤0.50mm; N≤4; DS≥10mm. 3.D > 0.50mm, Not allowable (不允许) . | | MI |
| | Linear foreign bodies 线状异物 | Under 6 inches 6寸以下 | 1.W≤0.05mm; L≤5mm, Ignore (忽略) . 2.0.05mm < W≤0.10mm ; L≤5mm ; N≤2; DS≥10mm. 3.0.10mm < W; 5mm < L, Not allowable (不允许) . | | MI |
| | | 6~10.0 inches 6寸~10.0寸 | 1.W≤0.07mm; L≤5mm, Ignore (忽略) . 2.0.07mm < W≤0.12mm ; L≤10mm ; N≤3 ; DS≥10mm. 3.0.12mm < W; 10mm < L, Not allowable (不允许) . | | MI |
| | | More than 10 inches 10寸以上 | 1.W≤0.10mm; L≤5mm, Ignore (忽略) . 2.0.10mm < W≤0.15mm ; L≤10mm ; N≤4 ; DS≥10mm. 3.0.15mm < W; 10mm < L, Not allowable (不允许) . | | MI |

| Material 检验区域 | Inspection items 检验项目 | Product size 适用尺寸 | Inspection standards 检验规格 | Category 缺陷类别 | |
|----------------------|---|----------------------|--|------------------|----|
| Display Screen 模组 | White/Black print 白印/黑印 | Full size 全尺寸 | Use ND5% filter masking, visual invisibility is OK 使用 ND5%遮盖, 目视不可见即为 OK. | | MI |
| | Interference pattern/Newtonian pattern 干涉纹/牛顿纹 | Full size 全尺寸 | Not allowable 不允许. | | MI |
| | Membranes displacement 膜材移位 | Full size 全尺寸 | Not allowable 不允许. | | MI |
| | Color blocks 色斑 | Full size 全尺寸 | Use ND5% filter masking, visual invisibility is OK /使用 ND5%遮盖, 目视不可见即为 OK. | | MI |
| | Display abnormal 画异 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | No display 无显示 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | Line/Missing Drawing 线条/缺画 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | Splash screen 闪屏 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | LCD grid LCD 网格 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | Afterimage 残影 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | Wrong viewing angle 可视角错误 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| TP | No touch 无触摸 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | Touch the jump point 触摸跳点 | Full size 全尺寸 | Not allowable 不允许. | MA | |
| | Touch not sensitive 触摸不灵敏 | Full size 全尺寸 | Not allowable 不允许. | MA | |