

# Shenzhen Leadtek Electronics Co.,Ltd

## PRODUCT SPECIFICATION

### TFT-LCD MODULE

**Module No:** LTK050FTBIT47-V0

Preliminary Specification

Approval Specification

Designed by	Checked by	Approved by
<i>jona</i>	<i>Jams</i>	<i>lan</i>

### Final Approval by Customer

Approved by	Comment

※The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.



## 2.General Specifications

NO	Item	Specification	Unit	Remark
1	LCD Size	TFT" 5.0	inch	-
2	Panel Type	IPS	-	-
3	Display Resolution	720 x RGB x 1280	pixel	-
4	Display Mode	Normally Black	-	-
5	Number of Colors	16.7M	-	-
6	Viewing Direction	ALL	-	-
7	CTP+LCM Module Size	98.70(H) x176.00(V) x30.9(T)	mm	Note
8	Panel Active Area	61.78 (H) x 109.82 (V)	mm	Note
9	Pixel Pitch	0.0858(H) x 0.0857(V)	mm	-
10	Weight	-	g	-
11	Driver IC	ST7123	-	-
12	Light Source	White LED	-	-
13	Interface	MIPI	-	-

Note : Please refer to the mechanical drawing;

## 3.Mechanical Drawing



## 4. Interface Specification

No	Symbol	Function
1	GND	Power ground.
2	TOUCH_3.3V	Power Supply For CTP.
3	GND	Power ground.
4	TOUCH_INT	Interrupt request to the host, or Wakeup request from the host.
5	TOUCH_RST	External Reset, Low is active.
6	TOUCH_SDA	I2C data input and output.
7	TOUCH_SCL	I2C clock input.
8	GND	Power ground.
9	LED-	Power supply for backlight cathode input terminal
10	LED+	Power supply for backlight anode input terminal.
11	ID	ID PIN Pull High, If not use, please not connect)
12	RESET	Reset signal (Low: Active).
13	NC	No connect
14	IOVCC_1.8V	I/O Voltage : 1.65V ~ 3.3V
15	VCC_2.8V	Power supply : 2.8V ~ 3.3V
16	GND	Power ground.
17	D3P	MIPI_DP3+ are differential data signal line
18	D3N	MIPI_DP3- are differential data signal line
19	GND	Power ground.
20	D2P	MIPI_DP2+ are differential data signal line
21	D2N	MIPI_DP2- are differential data signal line
22	GND	Power ground.
23	CLKP	CLOCK Lane positive-end input pin
24	CLKN	CLOCK Lane engative-end input pin
25	GND	Power ground.
26	D1P	MIPI_DP1+ are differential data signal line
27	D1N	MIPI_DP1- are differential data signal line
28	GND	Power ground.
29	D0P	MIPI_DP0+ are differential data signal line
30	D0N	MIPI_DP0- are differential data signal line
31	GND	Power ground.

## 5. Electrical Characteristics

### 5.1 TFT DC Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage for I/O	IOVCC	1.65	1.8	1.95	V
Supply Voltage for(DC/DC)	VCC	2.8	3.3	3.3	V

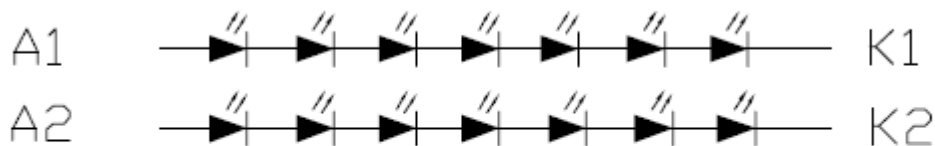
### 5.2 Back-light Unit:

PARAMETER	Sym.	Min.	Typ.	Max.	Unit	Test Condition	Note
LED Current	IF	–	40	–	mA	–	–
LED Voltage	VF	–	21	23.1	V	I=40mA	–
Life Time		–	30000	–	Hr.	I ≤ 40mA	–
CG+LCM Brightness	Luminance	–	500	--	cd/m <sup>2</sup>	I=40mA	
Color	White						

Note (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

(2) Ta=25±2°C

(3) Test condition: LED Current 40mA



$$I_F = 40\text{mA}, V_F = 21\text{V (TYP)}$$

## 6.0 ELECTRICAL CHARACTERISTICS

### 6.1 Absolute Maximum Ratings

Item	Symbol	Range	Unit
Supply Voltage (Analog)	IOVCC	- 0.3 ~ +2.1	V
Supply Voltage (I/O)	DPHYVCC	- 0.3 ~ +2.1	V
Driver Supply Voltage	AVDD-VSS	-0.3 ~ +6.6	V
Driver Supply Voltage	AVEE-VSS	- 6.6 ~ +0.3	V
Driver Supply Voltage	VGH-VGL	-0.3 ~ +30.0	V
Logic Input Voltage Range	VIN	-0.3 ~ DPHYVCC + 0.3	V
Logic Output Voltage Range	VO	-0.3 ~ DPHYVCC + 0.3	V
Operating Temperature Range	TOPR	-30 ~ +75	°C
Storage Temperature Range	TSTG	-40 ~ +110	°C

## 6.2 DC Characteristics

### 6.2.1 Basic Characteristics

Parameter	Symbol	Condition	Specification			Unit	Note
			MIN.	TYP.	MAX.		
Power & Operation Voltage							
Power supply Voltage	IOVCC		1.65	1.8	1.95	V	
	DPHYVCC		1.65	1.8	1.95	V	
	AVDD		4.5	5.9	6.3	V	
	AVEE		4.5	5.9	6.3	V	
Gate Driver High Voltage	VGH		7		18	V	Note 2
Gate Driver Low Voltage	VGL		-7		-18	V	
Input / Output							
Logic-High Input Voltage	VIH		0.7VDDI		VDDI	V	Note 1
Logic-Low Input Voltage	VIL		VSS		0.3VDDI	V	Note 1
Logic-High Output Voltage	VOH	IOH = -1.0mA	0.8VDDI		VDDI	V	Note 1
Logic-Low Output Voltage	VOL	IOL = +1.0mA	VSS		0.2VDDI	V	Note 1
Input Leakage Current	IIL	VIN=IOVCC/VSS	-1		+1	uA	Note 1
VCOM Voltage							
VCOM amplitude	VCOM		-2.75		-0.2	V	
Source Driver							
Gamma Amplitude (Positive)	VGPAMP		3		6	V	
Gamma Amplitude (Negative)	VGNAMP		-6		-3	V	

## 6.2.2 MIPI DC Characteristic

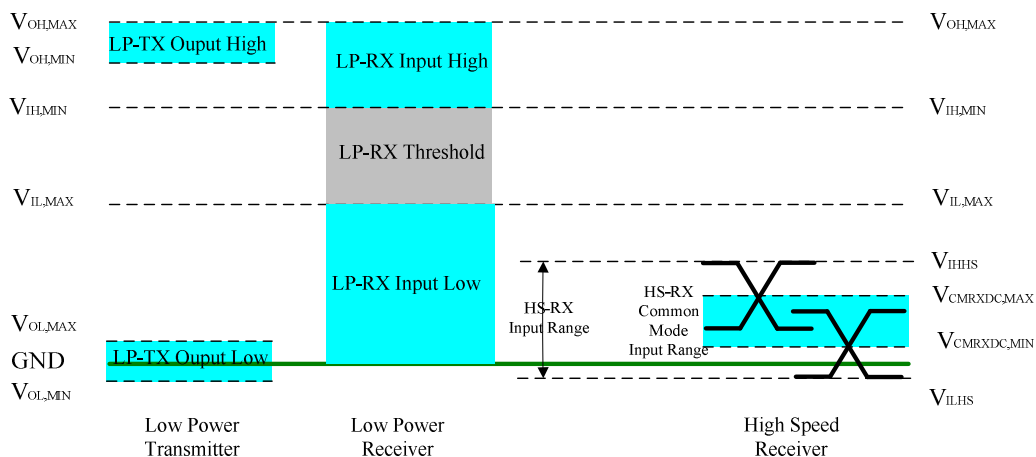


Figure 77 MIPI Signaling Voltage Levels

Parameter	Symbol	Specification			Unit
		MIN	TYP	MAX	
<b>Operation Voltage for MIPI Receiver</b>					
Low power mode operating voltage	VLPH	-	1.2	-	V
<b>MIPI Characteristics for High Speed Receiver</b>					
Single-ended input low voltage	VILHS	-40	-	-	mV
Single-ended input high voltage	VIHHS	-	-	460	mV
Common-mode voltage	VCMRXDC	70	-	330	mV
Differential input impedance	ZID	80	100	125	ohm
<b>MIPI Characteristics for Low Power Mode</b>					
Pad signal voltage range	VI	-50	-	1350	mV
Logic 0 input threshold	VIL	0	-	550	mV
Logic 1 input threshold	VIH	880	-	-	mV
Output low level	VOL	-50	-	50	mV
Output high level	VOH	1.1	1.2	1.3	V

### 6.3 AC Characteristics

#### 6.3.1 MIPI Timing

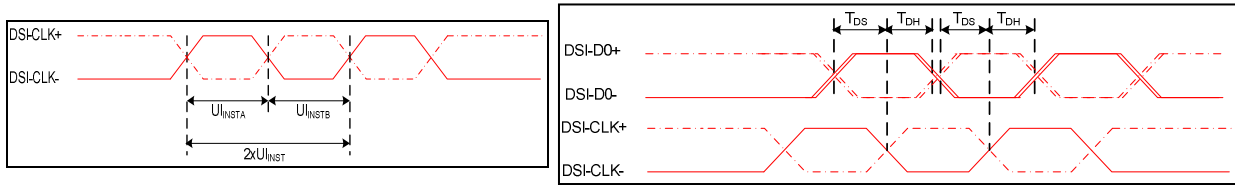


Figure 78 High Speed Mode – Clock Channel Timing

Signal	Symbol	Parameter	Specification			Unit	Description
			MIN	TYP	MAX		
DSI-CLK+/-	$2 \times U_{INST}$	Double UI instantaneous	1.66	-	10	ns	
DSI-CLK+/-	$U_{INSTA}, U_{INSTB}$	UI instantaneous half	0.83	-	5	ns	
fDSI-CLK+/-	$F_{DSICLK}$	DSI-CLK+/- frequency	100	-	625	MHz	
DSI-Dn+/-	$T_{DS}$	Data to clock setup time	0.15	-	-	UI	
DSI-Dn+/-	$T_{DH}$	Data to clock hold time	0.15	-	-	UI	

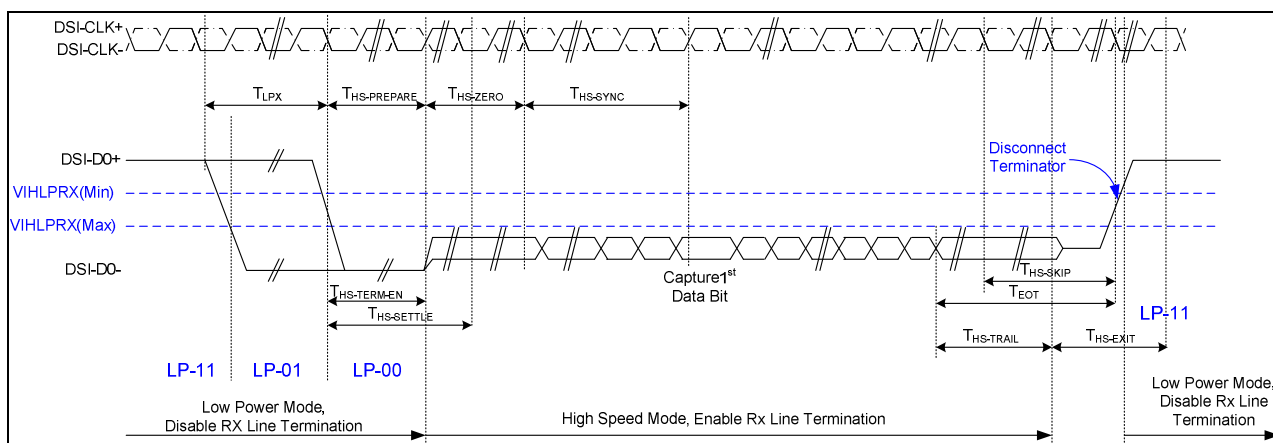


Figure 79 High-Speed Data Transmission

Parameter	Symbol	Specification			Unit
		MIN	TYP	MAX	
Time to drive LP-00 to prepare for HS transmission	$T_{HS-PREPARE}$	40+4UI	-	85+6UI	ns
Time from start of $t_{HS-TRAIL}$ or $t_{CLK-TRAIL}$ period to start of LP-11 state	$T_{EOT}$	-	-	105+12UI	ns
Time to enable data receiver line termination measured from when $D_n$ crosses $V_{ILMAX}$	$T_{HS-TERM-EN}$	-	-	35+4UI	ns
Time to drive flipped differential state after last payload data bit of a HS transmission	$T_{HS-TRAIL}$	60+4UI	-	-	ns
Time-out at RX to ignore transition period of EoT	$T_{HS-SKIP}$	40	-	145+10UI	ns
Time to drive LP-11 after HS burst	$T_{HS-EXIT}$	100	-	-	ns
Length of any Low-Power state period	$T_{LPX}$	50	-	-	ns
Sync sequence period	$T_{HS-SYNC}$	-	8UI	-	ns
Minimum lead HS-0 drive period before the Sync sequence	$T_{HS-ZERO}$	105+6UI	-	-	ns
Time interval during which the HS receiver should ignore any Clock Lane HS transitions, starting from the beginning of $T_{CLK-PREPAR}$	$T_{CLK-SETTLE}$	95	-	300	ns
Time interval during which the HS receiver shall ignore any Data Lane HS transitions, starting from the beginning of $T_{HS-PREPAR}$ . The HS receiver shall ignore any Data Lane transitions before the minimum value, and the HS receiver shall respond to any Data Lane transitions after the maximum value.	$T_{HS-SETTLE}$	85+6UI	-	145+10UI	ns

## 7.0 OPTICAL SPECIFICATIONS

### 7.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance <math><1\text{lux}</math> and temperature=\theta and  $\Phi$  equal to  $0^{\circ}$ . We refer to  $\theta=0$  ( $=\theta_3$ ) as the 3 o'clock direction (the "right"),  $\theta=90$  ( $=\theta_{12}$ ) as the 12 o'clock direction ("upward"),  $\theta=180$  ( $=\theta_9$ ) as the 9 o'clock direction ("left") and  $\theta=270$  ( $=\theta_6$ ) as the 6 o'clock direction ("bottom"). While scanning  $\theta$  and/or  $\Phi$ , the center of the measuring spot on the Display surface shall stay fixed.

The backlight should be operating for 30 minutes prior to measurement.

### 5.2 Optical Specifications

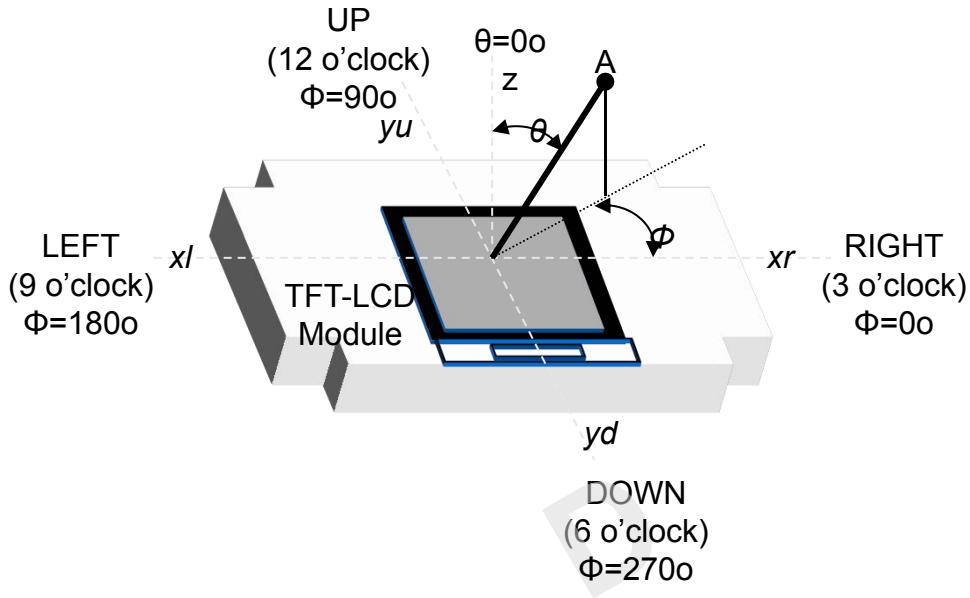
<Table 6. Optical Specifications>

[ $T_a=25 \pm 2^{\circ}\text{C}$ ]

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	$\theta_3$	CR > 10	80	85	-	Deg.	Note5.1
		$\theta_9$		80	85	-	Deg.	
	Vertical	$\theta_{12}$		80	85	-	Deg.	
		$\theta_6$		80	85	-	Deg.	
Contrast		CR	$\theta = 0^{\circ}$	1000	1500	-	-	HC+APF @silicate BLU ESR反射片No te 5.2/5.3
Cell Transmittance		Tr	-	4.3	5.1	-	%	
Color Gamut	NTSC	CIE1931	$\theta = 0^{\circ}$	63	68	-	%	
Chroma@CIE 1931	Red	(Rx,Ry)	$\theta = 0^{\circ}$	Typ- 0.03	(0.661,0.324)	Typ+ 0.03	-	CF@C Light Note 5.4
	Green	(Gx,Gy)	$\theta = 0^{\circ}$		(0.272,0.585)		-	
	Blue	(Bx,By)	$\theta = 0^{\circ}$		(0.136,0.122)		-	
	White	(Wx,Wy)	$\theta = 0^{\circ}$		(0.294,0.334)		-	
Response Time		Tr+Tf	$T_a= 25^{\circ}\text{C}$ $\theta = 0^{\circ}$	-	25	30	ms	Note 5.5

**Note 5.1:** Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see Figure 5).

**<Figure 5. Viewing Angle Range Is Defined As Follows>**



**Note 5.2:** Contrast measurements shall be made at viewing angle of  $\Theta=0^\circ$  and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

## 9. Reliability test items

No.	Test Item	Test Condition	Notes
1	High Temp. Storage	+80°C / 48H	1. Functional test isOK. Missing Segment,short, unclear segment non-display,display abnormally and liquid crystal leakare un-allowed. 2. No low temperature bubbles,end seal loose andfall, frame rainbow.
2	Low Temp. Storage	-30°C / 48H	
3	High Tempe. Operating	+70°C / 48H	
4	Low Tempe. Operating	-20°C / 48H	
5	High Temperature /Humidity storage	50°C x 90%RH /48H	
6	Thermal and cold shock	Static state, -20°C (30min) ~60°C (30min), 50 cycles	
7	ESD test	±2KV,Human Body Mode, 150pF/330Ω; ±4KV, Air Mode, 150pF/330Ω;	

Note: All tests above are practiced at module type.

There is no display function NG issue occurred, All the cosmetic specification is judged before the reliability stress.

## 10.0 General Precaution

### 10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

### 10.2 Assembly Precaution

- 1、 Please use the mounting hole on the module side in installing and do not bending or wrenching LCD in assembling. And please do not drop, bend or twist LCD module in handling.
- 2、 Please design display housing in accordance with the following guide lines.
- 3、 Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
- 4、 Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. The clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- 5、 Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)
- 6、 Please do not press any parts on the rear side such as source IC, gate IC, and FPC during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- 7、 Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- 8、 Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change. 11.2.7 Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

### 10.3 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. Leadtek does not warrant the module, if customers disassemble or modify the module.

### 10.4 Breakage of LCD Panel

- 1、 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 2、 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 3、 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 4、 Handle carefully with chips of glass that may cause injury, when the glass is broken.

## 10.5 Absolute Maximum Ratings and Power Protection Circuit

- 1、 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 2、 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 3、 It's recommended employing protection circuit for power supply.

### 11.6 Operation

- 1、 Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 2、 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 3、 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 4、 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

## 10.6 Static Electricity

- 1、 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 2、 Because LCD module uses CMOS-IC on TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- 3、 Persons who handle the module should be grounded through adequate methods.

## 10.7 Disposal

When disposing LCD module, obey the local environmental regulations.

## 10.8 OTHERS

- 1、 A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight land strong UV rays.
- 2、 Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.
- 3、 For the packaging box, please pay attention to the followings:
- 4、 Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
- 5、 Please do not pile them up more than 6 boxes. (They are not designed so.) And please do not turn over.
- 6、 Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
- 7、 Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)

## 11.0 Packing form-TBD



## 1.Scope of application /适用范围.

This document shall be applied to 0.95~5.0 inch touch display screen.

本文件适用于0.95~5.0 寸触摸显示屏.

## 2.Inspection conditions and environment /检验条件与环境.

### 2.1 Inspection Conditions /检验条件:

(1) Inspection Distance /检测距离: 35cm ±5cm.

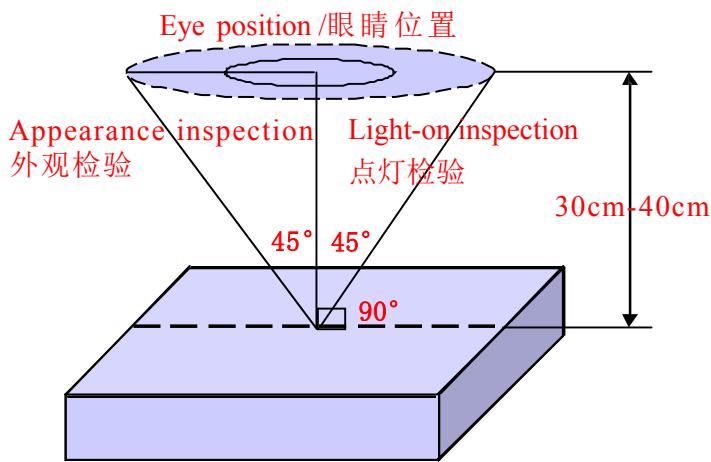
(2) Check time /检验时间:

Displays performance test /功能测试: 2~3S /Image, Cosmetic Inspection /外观检验:10~12S.

(3) Check the viewing angle /检验视角:

Light-on Inspection Angle /点灯检验角度: ±45°.

Cosmetic Inspection Angle /外观检验角度: ±45°.



(Perpendicular to LCD panel surface /垂直于LCD表面)

### 2.2 Inspection environment /检验环境:

Ambient Temperature 温度		25°C±5°C
Ambient Humidity 湿度		55±5%RH
Ambient Illumination 亮度	Cosmetic Inspection 外观检验	800-1000 Lux
	Functional Inspection 点灯检验	200~300Lux

### 2.3 Sampling Conditions /抽样条件:

(1) Quantity to be inspected /批量: Quantity of shipment lot per model /单次运送单一型号数量.

## (2) Sampling method /抽样方法:

Sampling Plan /抽样计划		GB/T 2828.1- 2003
		Normal Inspection , Single Sampling 正常检验、单次抽样
		General inspection level: II 一般检验水平: 二级
AQL	Major Defect /主要缺陷	0.65
	Minor Defect /次要缺陷	1.0

(3) The classification of Major(MA) and Minor(MI) defects is shown as “3.1 Classification of defects” .  
主缺 (MA) 及次缺 (MI) 定义于”3.1缺陷分类”.

## 3.Terms And Definitions /术语和定义

## 3.1 Classification of defects / 缺陷分类 :

## (1) Major defects /主要缺陷:

A major defect is a defect that is likely to result in failure, or to reduce materially the usability of the product for its intended purpose .

可导致产品功能失效或减少产品可用性的缺陷.

## (2) Minor defects /次要缺陷:

It will not cause the product to fail and reduce the defects in the effective use and operation of the product.

不会导致产品功能失效和减少产品的有效使用与操作的缺陷.

## 3.2 Point defects /点状缺陷:

The size of the point defect is defined by the diameter D, and the average diameter of the defect is

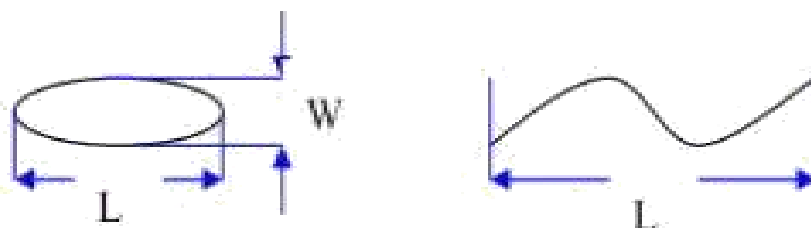
$$D=1/2 (W+L) .$$

点状缺陷的大小是由直径 D 定义的, 缺陷的平均直径  $D=1/2(W+L)$ .

## 3.3 Linear defects /线状缺陷:

When defect size  $L \geq 2W$ , the defect count as liner type defect. Size of linear defect is defined by length ( L ) and the maximum width ( W ).

当缺陷尺寸  $L \geq 2W$  时, 被视为线状缺陷, 线状缺陷是由长度 (L) 和最大宽度 (W) 定义的.



### 3.4 LCD sub-pixel dot /LCD子像素点

(1) Definition /定义 : The point defect area is greater than 50% of the LCD sub-pixel area, and is visible through ND5% filter masking .

子像素点缺陷面积大于 50% LCD子像素面积, 且透过 ND5%遮盖是可见的.

(2) The drawing of 1/2 area sub-pixel definition / 1/2 面积的子像素定义绘图:

The 1/2 area sub-pixel can be defined as below one or more of specific shapes

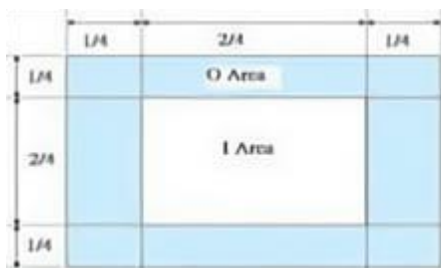
1/2 面积的子像素可以定义为如下一个或多个特定形状图:



### 3.5 Small bright dot /细碎亮点 :

Point defects smaller than "LCD sub-pixels" /小于“LCD子像素点”的点缺陷.

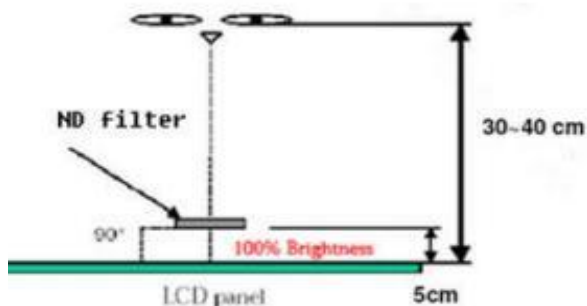
(Ratio of Zone I to Zone O / I 区与 O 区比例: 1: 2: 1)



### 3.6 ND filter inspection method /ND卡的检验方法:

Hold the ND filter about 5cm above the display area, with your eyes 30-40cm away from the panel, and observe for 2~3 seconds.

在显示区域上方大约 5cm 处握住 ND 卡, 眼睛距离面板 30-40cm, 观察2~3 秒.



3.7 Any FPC surface problems that do not leak copper on the surface and do not cause functional failure are acceptable.

任何 FPC 表面问题, 表面未露铜和不造成功能失效是可以接受的.

3.8 Extraneous substances that can be wiped out , like Finger point,Particles are not considered as a defect .

可以被擦拭干净的表面物质不视为缺陷 (如手指印, 尘粒) .

3.9 Defects that are covered by the material and are not visible to the eye and do not affect the function and use are not considered defects.

会被物料覆盖目视不可见，且不影响功能与使用的缺陷不视为缺陷。

### 3.10 Panel damage /面板损伤:

Glass damage outside the AA display area that does not affect the effective wiring is acceptable.

AA 显示区域以外的玻璃损伤，不影响有效线路是可以接受的。

3.11 Issues not specified or defined in this acceptance standard shall be handled through friendly negotiation between the two parties.

本允收标准中未规定或定义的问题，双方友好协商处理。

## 4. Inspection standards /检验标准

### 4.1 Structural Dimensions /结构尺寸规格

Serial Number 序号	Measurement items /测量项目		Specification /规格	Remark /备注
	名称 /Name	Unit /单位	Tolerance /公差	
1	Outside dimension: Length 尺寸: 长	mm /毫米	0.10mm~0.20mm	Please refer to the product specification for detailed dimensions and tolerances 详细的尺寸规格和公差请参考产品规格书
2	Outside dimension: Width 尺寸: 宽	mm /毫米	0.10mm~0.2mm	
3	Outside dimension: Thickness 尺寸: 厚	mm /毫米	0.20mm~0.30mm	

### 4.2 Appearance Inspection Specification /外观检验规格

(D : diameter, W : width, L : length, N : quantity, DS : spacing)

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass 玻璃	Wire(on Array) 线路	Can't be damaged 不能损伤	MA	
	Chipping/corner breaking 崩边/破角	Can't affect the effective lines and functions 不能影响有效线路和功能	MA	
	Edge 边缘	There must be no extensional cracks 不可有延伸性裂纹	MA	
Silicone 硅胶	Silicone coating 硅胶涂布	The height must not exceed the LCD CF surface 高度不能超过LCD CF面		MI
	Glue overflow 溢胶	Can't cover FPC, POL, etc 不能覆盖到FPC、POL等		MI

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
PCBA FPC Connector 连接器	Appearance 外观	Scratches or injuries are not allowed to cause copper exposure 划伤或损伤不允许表面出现露铜		MI
	Component 元器件	Can't be damaged and lack 不能损伤和缺少	MA	
	Gold finger oxidation 金手指氧化	Not allowed 不允许		MI
	Connection status 连接状况	The connection must be accurate and stable 必须准确稳定连接	MA	
	Break 破裂	Not allowed 不允许	MA	
	Soldering.: false soldering/tinning/tin beads 假焊/连锡/锡珠	Not allowed 不允许	MA	
POL 偏光片	Scratches 划伤	1. $W \leq 0.05\text{mm}$ ; $L \leq 5\text{mm}$ , Ignore (忽略) 2. $0.05\text{mm} < W \leq 0.10\text{mm}$ ; $L \leq 5\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.10\text{mm} < W$ ; $5\text{mm} < L$ , Not allowable (不允许)		MI
	Dent 凹凸印	1. $D \leq 0.15\text{mm}$ , Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$ , Not allowable (不允许)		MI
	Bubbles 气泡	1. $D \leq 0.15\text{mm}$ , Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$ , Not allowable (不允许)		MI
	Point defects 点状不良	1. $D \leq 0.15\text{mm}$ , Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$ , Not allowable (不允许)		MI
	Edge bubbles 边缘气泡	1. Within 1/2BM of the display area, it is not allowed 显示区往外 1/2BM 区域内, 不允许 2. The display area is 1/2 outside the BM area, and it is not controlled 显示区往外 1/2BM 区域以外, 不管控		MI
	Dirty/watermarked 脏污/水印	No dirt/water lines/finger marks are allowed, and must be wiped clean 不允许有脏污/水印/手指印, 须擦拭干净方可		MI
	Warping 起翘	Not allowed 不允许		MI
	Attaching offset 贴偏	It is necessary to completely cover the display area outward, within the 1/2BM area, or without leaking POL edges after TP is attached 需完整覆盖显示区往外、1/2BM 区以内或贴合 TP 后不会出现漏偏光片边缘		MI
Mixture 混料	Mixing different types of POL or not using POL as required by the BOM, not allowed 不允许混贴不同型号的 POL 或未按 BOM 要求使用 POL	MA		

Inspection area 检验区域	Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
TP&CG	Point defects 点状不良	1. $D \leq 0.15\text{mm}$ , Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$ , Not allowable (不允许)		MI
	Scratches 划伤	1. $W \leq 0.05\text{mm}$ ; $L \leq 5\text{mm}$ , Ignore (忽略) 2. $0.05\text{mm} < W \leq 0.10\text{mm}$ ; $L \leq 5\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.10\text{mm} < W$ ; $5\text{mm} < L$ , Not allowable (不允许) 4. There is a feeling scratch, Not allowable 有感划伤, 不允许		MI
	Edges and corners cracked 崩角/崩边	1. Product front / 产品正面: Edge and corner chipping is not allowed 崩角、崩边不允许 2. Product back / 产品背面: $X \leq 0.5$ , $Y \leq 0.5$ , $Z \leq 1/2T$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$		MI
	Silk screen 丝印	The silk screen is clear, complete and correct 丝印清晰、完整、内容正确		MI
	Dirty 脏污	Non-wipeable dirt, not allowed 不可擦拭的脏污, 不允许		MI
	Broken 破损	Not allowable 不允许	MA	
	Ink color aberration 油墨色差	$\Delta E > 1$ , Not allowable (不允许)		MI
	Cover pinholes 针孔	1. $D \leq 0.10\text{mm}$ , $N \leq 3$ , $DS \geq 10\text{mm}$ , allowable 2. $D > 0.10\text{mm}$ , intensive pinholes (密集型针孔), Not allowable (不允许)		MI
	IR holes IR孔	Dirt, deviation, color difference, etc. are not allowed 不允许脏污、偏位、色差等		MI
BL 背光	Backlight separation 背光分离	Not allowable 不允许		MI
	Deformation of rubber iron and rubber frame 胶铁、胶框变形	Use the plug gauge 0.3mm on the flat surface and can snap in and judge NG 在平面上使用塞规0.3mm卡翘曲位置, 能卡进判定NG		MI
	The iron frame is oxidized and not tightened 铁框氧化、卡不紧	Not allowable 不允许		MI
	Backlight sticky solder beads, glue, etc 背面粘锡珠、残胶等	Not allowable 不允许		MI
	Inkjet coding, Barcode, QR code 喷码/条码/二维码	The Inkjet coding is clear and complete, the barcode and QR code can be scanned normally, and the content and format match 喷码清晰完整、条码和二维码可正常扫描, 内容和格式相符		MI
	Accessories (protective film, double-sided tape, insulating adhesive, etc.) 辅料(保护膜、双面胶、绝缘胶等)	Defects such as missing pastes, sticking deviations, defects, and fractures are not allowed 不允许有漏贴、贴偏、残缺、断裂等缺陷		MI

## 4.3 Electrical test specifications /电性检查规格

( D : diameter, W : width, L : length, N : quantity, DS : spacing )

Inspection items 检验项目	Inspection specifications 检验规格	Defect category 缺陷类别	
Glass bright spots/dark spots 玻璃亮点/暗点	1. $D \leq 0.15\text{mm}$ , Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$ , Not allowable (不允许)		MI
Mura	Use ND5% filter masking, visual invisibility is OK, 200~300Lux 使用ND5%遮盖, 目视不可见即为OK, 200~300Lux		MI
Small bright dot 细碎亮点	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
Light leakage 漏光	1. Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK 2. If necessary, sign off on the sample 必要时, 签限定样		MI
Backlight black/white dots 背光黑点/白点	1. $D \leq 0.15\text{mm}$ , Ignore (忽略) 2. $0.15\text{mm} < D \leq 0.30\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.30\text{mm} < D$ , Not allowable (不允许)		MI
Linear foreign bodies 线状异物 (异物毛丝等)	1. $W \leq 0.05\text{mm}$ ; $L \leq 5\text{mm}$ , Ignore (忽略) 2. $0.05\text{mm} < W \leq 0.10\text{mm}$ ; $L \leq 5\text{mm}$ ; $N \leq 3$ ; $DS \geq 10\text{mm}$ 3. $0.10\text{mm} < W$ ; $5\text{mm} < L$ , Not allowable (不允许)		MI
Black/White Print 黑印/白印	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The display is uneven 显示不均匀	Use ND5% filter masking, visual invisibility is OK 使用ND5%遮盖, 目视不可见即为OK		MI
The brightness is uneven 亮度不均匀	Brightness uniformity $< 85.0\%$ , Not allowable 亮度均匀性 $< 85.0\%$ , 不允许		MI
Displacement of the membrane 膜材移位	Not allowable 不允许		MI
Interference pattern/Newtonian pattern 干涉纹/牛顿纹	Not allowable 不允许		MI
Display abnormal 显示异常	Not allowable 不允许	MA	
No display 无显示	Not allowable 不允许	MA	
Line/Missing Drawing 线条/缺画	Not allowable 不允许	MA	
Splash screen 闪屏	Not allowable 不允许	MA	
LCD grid LCD网格	Not allowable 不允许	MA	
Afterimage 残影	Not allowable 不允许	MA	
Wrong viewing angle 视角错误	Not allowable 不允许	MA	
No touch 无触摸	Not allowable 不允许	MA	
Touch the jump point 触摸跳点	Not allowable 不允许	MA	
Not sensitive 触摸不灵敏	Not allowable 不允许	MA	